

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

2SK4177 — General-Purpose Switching Device Applications

Features

- ON-resistance RDS(on)= 10Ω (typ.)
- 10V drive

• Input capacitance Ciss=380pF (typ.)

Specifications

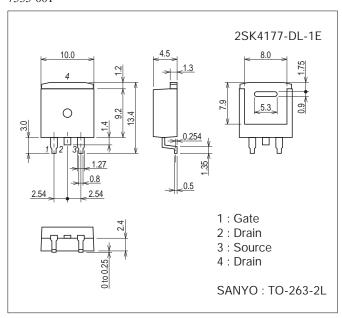
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		1500	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	ID		2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	4	А
Allowable Power Dissipation	PD	Tc=25°C	80	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		41	mJ
Avalanche Current *2	IAV		2	А

Note: *1 VDD=50V, L=20mH, IAV=2A (Fig.1)

Package Dimensions

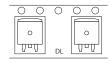
unit : mm (typ) 7535-001



Product & Package Information

Package : TO-263-2L
 JEITA, JEDEC : SC-83, TO-263
 Minimum Packing Quantity : 800 pcs./reel

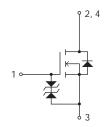
Packing Type: DL



K4177 O !LOT NO.;

Marking

Electrical Connection



^{*2} L≤20mH, single pulse

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	1500			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =1200V, V _{GS} =0V			100	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	2.5		3.5	V	
Forward Transfer Admittance	yfs	V _{DS} =20V, I _D =1A	0.7	1.4		S	
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =1A, V _G S=10V		10	13	Ω	
Input Capacitance	Ciss			380		pF	
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		70		pF	
Reverse Transfer Capacitance	Crss			40		pF	
Turn-ON Delay Time	t _d (on)			12		ns	
Rise Time	t _r	Son Fig 2		37		ns	
Turn-OFF Delay Time	t _d (off)	See Fig.2		152		ns	
Fall Time	t _f			59		ns	
Total Gate Charge	Qg			37.5		nC	
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =2A		2.7		nC	
Gate-to-Drain "Miller" Charge	Qgd			20		nC	
Diode Forward Voltage	V _{SD}	I _S =2A, V _{GS} =0V		0.88	1.2	V	

Fig.1 Unclamped Inductive Switching Test Circuit

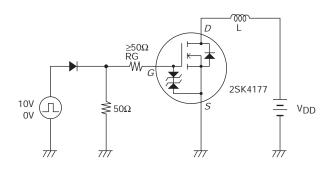
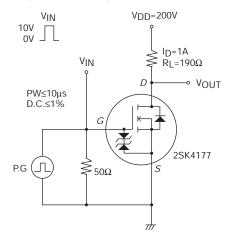
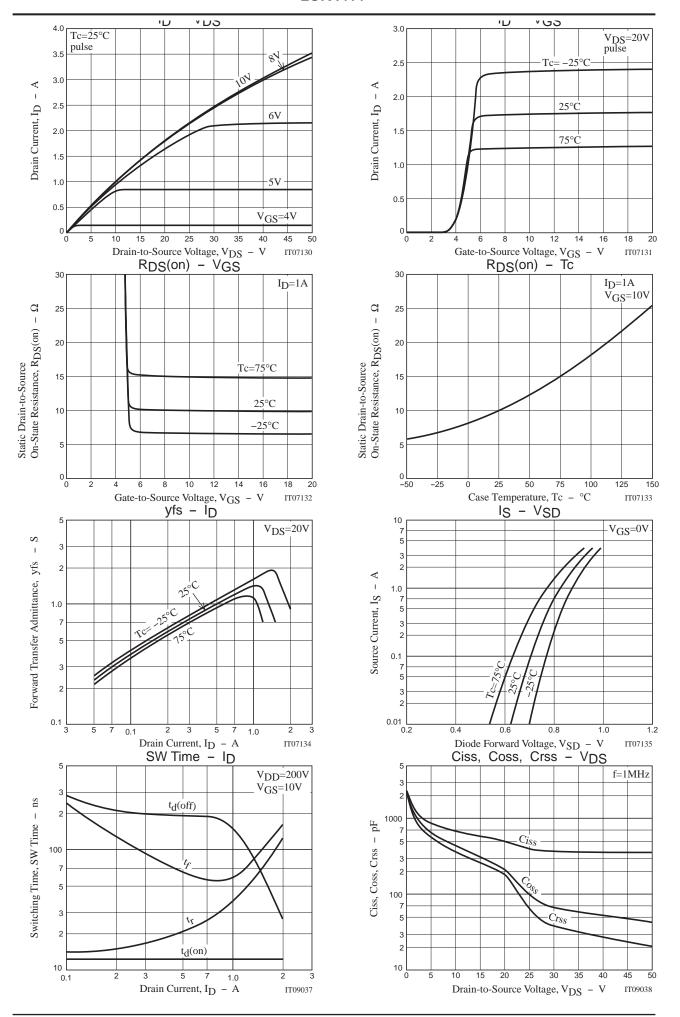


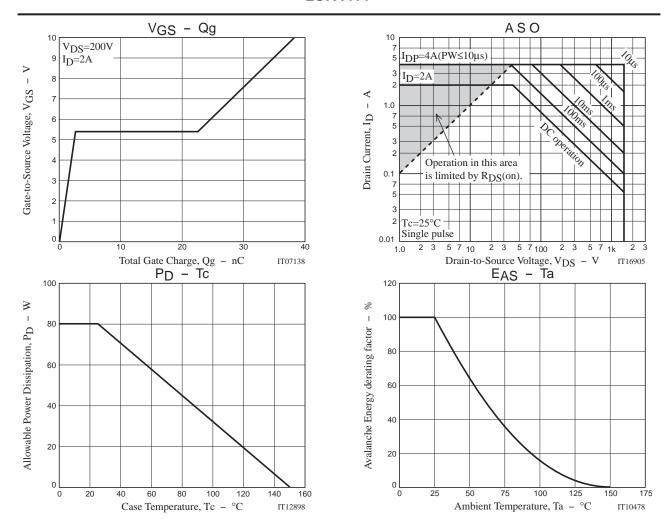
Fig.2 Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
2SK4177-DL-1E	TO-263-2L	800pcs./reel	Pb Free



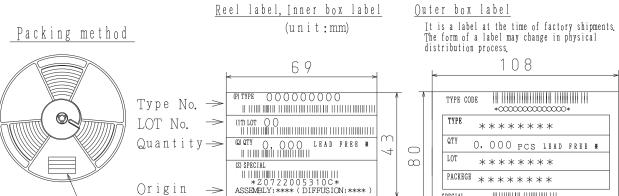


Taping Specification

2SK4177-DL-1E

1. Packing Format

Package	Name		Maximum Number of devices contained (pcs)		Packing format		
		Reel	Inner box	Outer box	Inner BOX	Outer BOX	
TO-263-	-2L	800	1600	6400	SPD-0V0011 2 reel contained Dimensions:mm (external) 351×340×68	SPD-0V0009 4 inner boxes contained Dimensions:mm (external) 390×370×318	



NOTE (1) The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Z0722005310C ASSEMBLY:**** (DIFFUSION:****)

SPECIAL

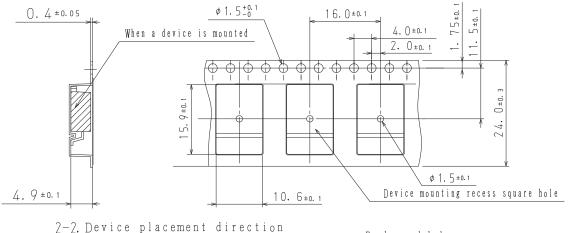
Label		JEITA Phase
LEAD FREE	3	JEITA Phase 3A

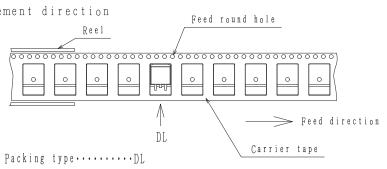
7. Taping configuration

2-1. Carrier tape size (unit:mm)

Origin

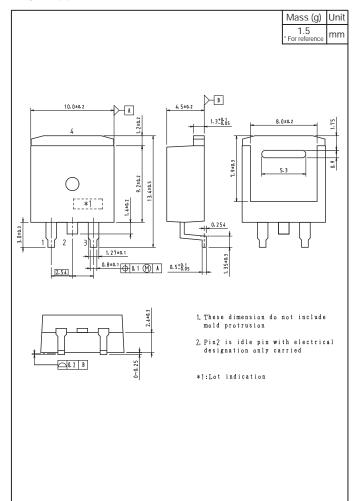
Reel label



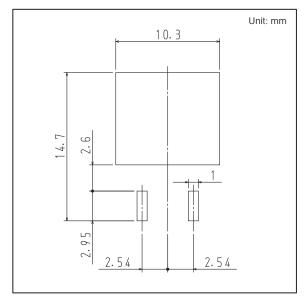


Outline Drawing

2SK4177-DL-1E



Land Pattern Example



Note on usage: Since the 2SK4177 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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